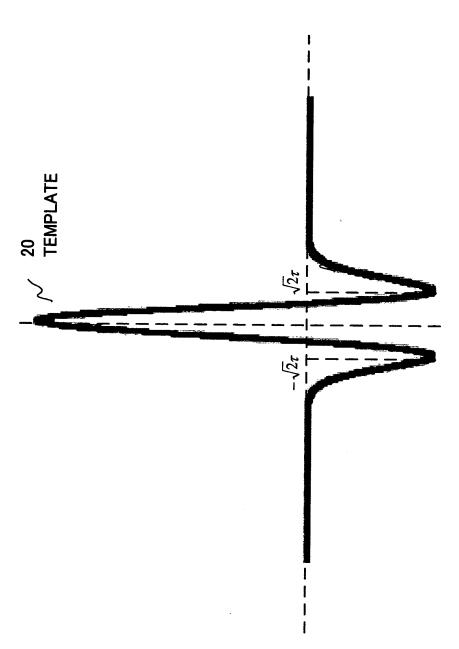


FIG. 1



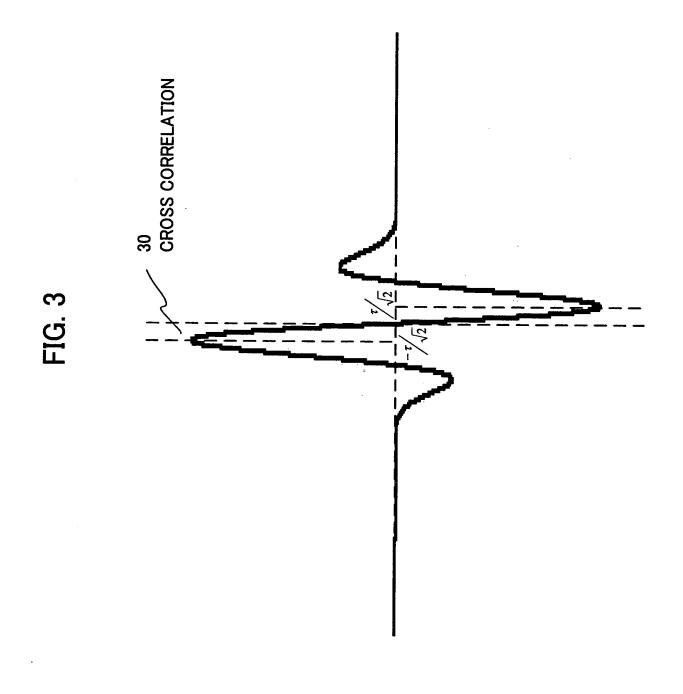
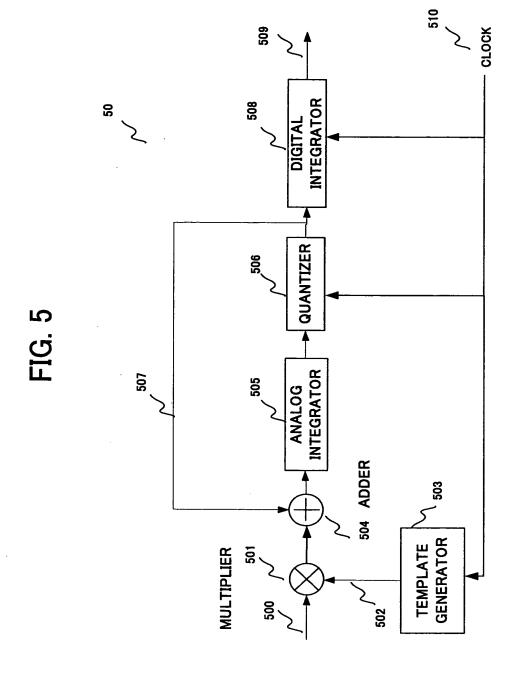
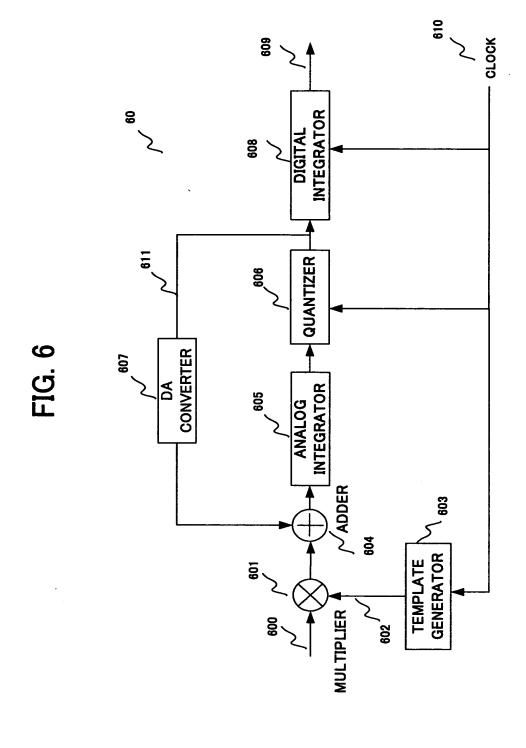


FIG. 4





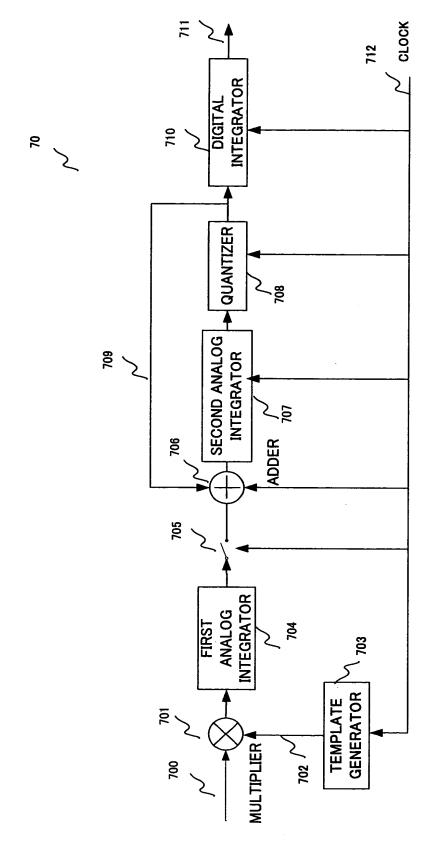
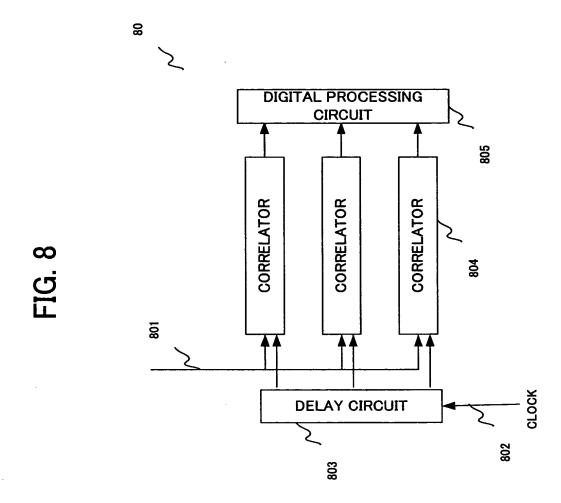
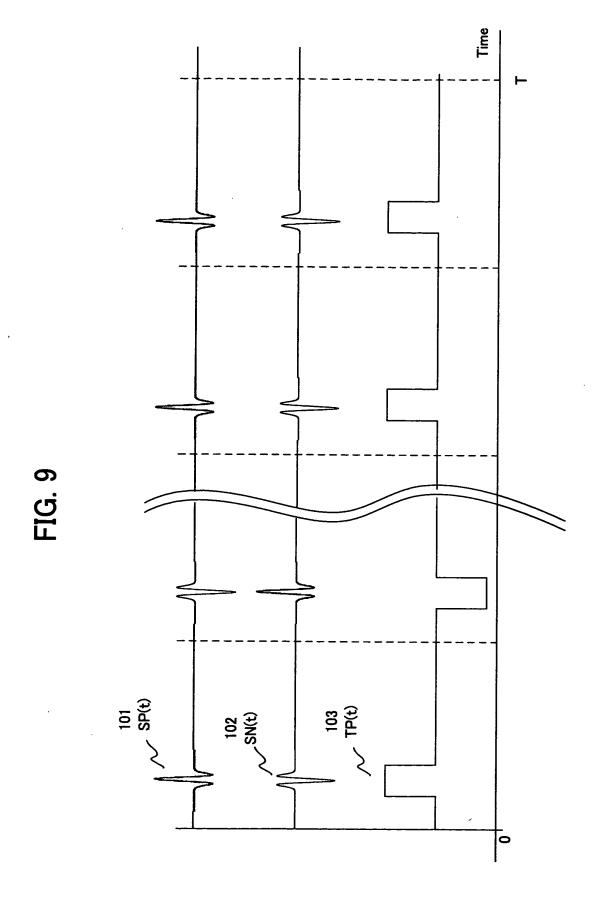


FIG. 7





COUNTER MIXER 00.47 ANALOG INTEGRATOR MEMORY PROGRAMMABLE DELAY TEMPLATE GENERATOR **623**2 % FINE DELAY SAMPLE DELAY LINE **PELAY** 0 PELAY PROGRAM COUNTER LOAD COUNT DELAY LINE LOAD DELAY COARSE DEL AY COUNTER DIGITAL INTEGRATOR CONTROL TRANSMITTER SEQUENCE CONTROL S S 00 D **OSCILLATOR** DISPL AY

FIG. 10 PRIOR ART